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**CONNECTOR FOR HIGH-SPEED COMMUNICATIONS**

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**FIELD OF THE INVENTION**

The invention relates in general to electrical connectors. More particularly, the invention relates to electrical connectors for high speed communications.

**15 BACKGROUND OF THE INVENTION**

Electrical connectors provide signal connections between electronic devices. Often, the signal connections are so closely spaced that undesirable cross talk occurs between nearby signals. That is, one signal induces electrical interference to a nearby signal. With electronic device miniaturization and high speed electronic communications becoming more prevalent, cross talk becomes a significant factor in connector design. In order to reduce cross talk between signals, it is known to provide grounding connection pins in such connectors. However, as communication speeds increase, wider signal conductors are typically used. With such wider signal conductors and conventional grounding, it becomes difficult to

provide both high signal contact pin density and acceptable cross talk levels.

Therefore, a need exists for electrical connectors for high speed communications having a high density of signal contact pins and acceptable cross talk levels.

## 5 SUMMARY OF THE INVENTION

The invention is directed to a high speed electrical connector.

An electrical connector is provided that comprises a substantially planar dielectric, a substantially planar ground plane, and a signal conductor. The ground plane is disposed on one planar surface of the dielectric and the signal conductor is disposed on the  
10 opposing planar surface of the dielectric.

The dielectric may comprise polyimide, a recess for receiving a solder ball for a ball grid array connection to a circuit card, and a finger extending substantially in the plane of the dielectric. Moreover, the signal conductor may extend along the finger.

The ground plane may comprise a plurality of ground contact pins extending  
15 from an end of the ground plane and the ground plane comprises phosphor bronze and may be plated and etched onto the dielectric.

The signal conductor may comprise a signal contact pin, may be plated and etched onto the dielectric, and may comprise a differential pair of signal conductors.

The electrical connector may comprise a plurality of connection modules  
20 wherein each module comprises a substantially planar dielectric, a substantially planar ground plane, and a signal conductor.

An electrical interconnection system is also provided. The electrical interconnection system comprises a header connector and a receptacle connector. The header

connector comprises a plurality of connection modules. Each module comprises a substantially planar dielectric, a substantially planar ground plane, and a signal conductor.

The ground plane is disposed on one planar surface of the dielectric and the signal conductor is disposed on the other planar surface of the dielectric. The receptacle comprises a plurality

5 of receptacle contacts for receiving the signal contact pins and the ground contact pins.

The foregoing and other features of the invention will become apparent from the following detailed description of the invention when considered in conjunction with the accompanying drawings.

## 10 BRIEF DESCRIPTION OF THE DRAWINGS

The invention is further described in the detailed description that follows, by reference to the noted drawings by way of non-limiting illustrative embodiments of the invention, in which like reference numerals represent similar parts throughout the drawings, and wherein:

15 Figure 1 is a perspective view of an illustrative electrical connector (without a housing) and illustrative receptacle, in accordance with an embodiment of the invention;

Figure 2 is a perspective view of a portion of the electrical connector of Figure 1;

20 Figure 3 is a cut-away view of the electrical connector of Figure 1 taken along line A-A;

Figure 4 is a perspective view of an illustrative pair of signal contact pins of the electrical connector of Figure 1;

Figure 5 is a perspective view of an illustrative ground plane of the electrical connector of Figure 1;

Figure 6 is a cut-away view of the electrical connector of Figure 1 taken along Line B-B; and

5 Figure 7 is a front view of the receptacle of Figure 1.

### DETAILED DESCRIPTION OF ILLUSTRATIVE EMBODIMENTS

The invention is directed to a high speed electrical connector comprising a substantially planar dielectric, a substantially planar ground plane, and signal conductor. The ground plane is disposed on one planar surface of the dielectric and the signal conductor is disposed on the other planar surface of the dielectric.

Certain terminology may be used in the following description for convenience only and is not considered to be limiting. For example, the words "left", "right", "upper", and "lower" designate directions in the drawings to which reference is made. Likewise, the words "inwardly" and "outwardly" are directions toward and away from, respectively, the geometric center of the referenced object. The terminology includes the words above specifically mentioned, derivatives thereof, and words of similar import.

Figure 1 is a perspective view of an illustrative electrical connector (without a housing) and illustrative receptacle, in accordance with an embodiment of the invention. As shown in Figure 1, connector 10 and receptacle 80 provide electrical connection between circuit board 90 and backplane 95. Connector 10 comprises a plurality of connection modules 20. Modules 20 may be contained in a housing (not shown) which may comprise molded thermoplastic or the like.

Each module 20 provides for electrical transmission of signals between circuit board 90 and backplane 95. As more signals are desired to be transmitted, more modules 20 may be added to connector 10. The number of signals depends in part on the type of data transmission.

5           One technique for transmitting data is common mode transmission, which is also referred to as single ended transmission. Common mode refers to a transmission mode which transmits a signal level that is compared to a reference voltage level, typically ground, that is common to other signals in the connector or transmission line. A limitation of common mode signaling is that common mode noise is often transmitted along with the signal.

10           Another technique of transmitting data is differential mode transmission. Differential mode refers to a transmission mode where a signal on one line of voltage  $V$  is referenced to a line carrying a complementary voltage of  $-V$ . Appropriate circuitry subtracts the lines, resulting in an output of  $V - (-V)$  or  $2V$ . Common mode noise is canceled at the differential receiver by the subtraction of the signals. This technique reduces transmission  
15 errors, thereby increasing possible communication speed; however, more signal conductors are used for differential mode transmission than for common mode transmission. That is, for differential mode transmission, two conductors are used for each signal - a positive signal conductor and negative signal conductor. In contrast, for common mode transmission, many signals may share a single conductor as their ground conductor. Therefore, selection of the  
20 method of transmission depends on the application. As shown and described, connector 10 employs differential mode transmission; however, connector 10 may also employ single ended transmission.

Figure 2 is a perspective view of a portion of module 20. As shown in Figure 2, module 20 comprises a ground plane 30, a dielectric 40, and a plurality of signal conductors 50.

As can be seen, conductors 50 are disposed on a planar surface of dielectric 40 and are employed as signal conductors of a differential pair. That is, one conductor 50 is employed as a positive signal conductor S+ and an adjacent conductor 50 is employed as a negative signal conductor S-. Conductors within a differential pair of signal conductors are located closer than conductors of two adjacent differential pairs. In this manner, cross talk between differential pairs may be reduced.

Further, conductors 50 are located such that connector 10 is a right angle connector; however, connector 10 may be a straight through connector. As a right angle connector, signal conductor 50 comprises a first section 51 and a second section 52 disposed approximately ninety degrees to first section 51. In this manner, connector 10 may be used to connect between electronic devices having mating surfaces orthogonal to each other.

An illustrative conductor 50 has a width of approximately 0.38 mm, a thickness of approximately 0.08 mm, and a pitch of approximately 1 mm; however, various conductor dimensions may be used.

Conductors 50 may be plated and etched onto dielectric 40. Plating and etching conductors 50 onto dielectric 40 may simplify manufacturing by reducing assembly time and eliminating over-molding time. Also, etching conductors 50, rather than stamping conductors 50 from a die, provides the capability to more easily change conductor impedances *i.e.*, by changing conductor size and/or spacing. That is, to manufacture a different size and/or spaced conductor, a stamped conductor may use a newly machined die. Such die machining

may take an unacceptable long time. Moreover, plating and etching conductors 50 onto dielectric 40 may provide precisely spaced and sized conductors, thereby allowing more control of electrical transmission characteristics and therefore, higher speed communications.

Dielectric 40 is substantially planar and may comprise polyimide or the like.

5 A low dielectric material is typically desired for high speed communications. Therefore, dielectric 40 may comprise polyimide; however, other materials may be used, typically, other low dielectric materials. An illustrative dielectric 40 is approximately 0.25 mm thick; however, various thicknesses may be employed depending on the desired impedance characteristics between conductors 50 and ground plane 30. Dielectric 40 comprises a recess  
10 42 at an end of its planar surface proximate to conductor 50 for receiving a solder ball for a ball grid array attachment, for example, of conductor 50 to circuit board 90. While solder ball connection of conductor 50 to circuit board 90 is illustrated, other techniques are contemplated.

Dielectric 40 comprises a finger 44, extending substantially in the plane of the  
15 dielectric, for each differential pair of signal conductors. Conductors 50 of a differential pair of signal conductors extend along finger 44. Finger 44 is for attachment of a signal contact 52 (Figure 4) to conductor 50.

Figure 4 is a perspective view of a pair of signal contacts 52. As shown in Figure 4, each signal contact 52 comprises a straight section 53, bowed section 54, an offset  
20 section 56, and a signal contact pin 58. Straight section 53 comprises a substantially straight conductor. Bowed section 54 comprises a bowed conductor for connection between straight section 53 and conductor 50. Offset section 56 comprises a substantially planar surface bent at approximately a right angle to offset signal contact pin 58 from the plane of straight section

53 for connection to receptacle 80. Contact pin 58 is shown with an aperture 59 for providing good contact with receptacle 80; however, contact pin 58 may be any suitable contact pin. Further, signal contact 52 may be any suitable contact. Signal contacts 52 may comprise phosphor bronze, beryllium copper, and the like.

5 Referring now to Figure 3, dielectric 40 is disposed between conductors 50 and ground plane 30. Figure 5 is a perspective view of ground plane 30. As shown in Figure 5, ground plane 30 is substantially continuous and planar and is disposed on one planar surface of dielectric 40. Ground plane 30 comprises apertures 32, offset sections 36 and ground contact pins 38. Apertures 32 are disposed between differential pairs of conductors  
10 50. The size of apertures 32 may be modified to achieve a desired impedance characteristic. Offset section 36 comprises a substantially planar surface bent at approximately a right angle to offset ground contact pin 38 from the plane of ground plane 30 for connection to receptacle 80. Ground contact pin 38 is shown with an aperture 39 for providing good contact with receptacle 80; however, contact pin 38 may be any suitable contact pin. Ground plane 30 may  
15 comprise phosphor bronze, beryllium copper, and the like.

Ground plane 30 and conductors 50 connect to receptacle 80 via ground contact pins 38 and signal contact pins 58, respectively. As such, and as illustrated in Figures 6 and 7, ground contact pins 38 and signal contact pins 58 are aligned with receptacle contacts 82.

20 As shown in Figure 6, signal contact pins 56 and ground contact pins 36 are arranged into a plurality of rows and columns. As can be seen, a row includes a repeating sequence of, from left to right, a positive signal conductor S+, a negative signal conductor S-, and a ground conductor G. Spacing between contact pins within a row may vary. For

example, spacing between positive signal conductor S+ and negative signal conductor S- is a distance D2, which may be about 2 mm. Spacing between signal conductors S+, S- and ground conductor G is a distance D3, which may be about 1.25 mm. Spacing between corresponding conductors of an adjacent module 20 is a distance D4, which may be about 4.5 mm. Distance between adjacent columns is a distance D1, which may be about 2.7 mm. A typical pitch is about 2.5 times the width of conductors S; however, the connector can be configured for maximum signal density per linear inch and maximum trace routing channels, depending on the needs of the application.

As shown in Figure 7, receptacles 82 are aligned to receive the appropriate signal contact pins 56 and ground contact pins 36. Receptacles 82 are illustrated as having a round cross section; however, it should be noted that the use of other shapes, such as rectangular, square, and the like, is also contemplated.

It is to be understood that the foregoing illustrative embodiments have been provided merely for the purpose of explanation and are in no way to be construed as limiting of the invention. Words which have been used herein are words of description and illustration, rather than words of limitation. Further, although the invention has been described herein with reference to particular structure, materials and/or embodiments, the invention is not intended to be limited to the particulars disclosed herein. Rather, the invention extends to all functionally equivalent structures, methods and uses, such as are within the scope of the appended claims. Those skilled in the art, having the benefit of the teachings of this specification, may affect numerous modifications thereto and changes may be made without departing from the scope and spirit of the invention in its aspects.